SUPPLEMENTAL APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Number::   10/086,129

Filing Date::   02-28-2002

Application Type::   Regular

Subject Matter::   Utility

Title::   Method and System for ~~Flexible~~ <u>Flexibly</u> Nesting JTAG Tap Controllers for FPGA-Based System-On-Chip (SoC)

Docket Number::   X-1069 US

Request for Early Pub?::   No

Request for Non-Pub?::   Yes

Total Drawing Sheets::   Eleven

Small Entity?::   No

Petition included?::   No

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Technology Center 2100

INVENTOR INFORMATION

Inventor Authority Type::   Inventor

Primary Citizenship Ctry::   US

Status::   Full Capacity

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CORRESPONDENCE INFORMATION

Correspondence Customer Number::   24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	

ASSIGNEE INFORMATION

Assignee Name::   Xilinx, Inc.

Street::   2100 Logic Drive

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State or Province::   California

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